

30979 U.S. PTO
10/07/2165

02/07/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10072165	02/07/2002	326	30	2819	D. C. G. G.

**APPLICANTS: Coughlin Terry; Wang Geoffrey;

**CONTINUING DATA VERIFIED:

None

DC

** FOREIGN APPLICATIONS VERIFIED:

None

DC

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no
35 USC 119 conditions met ☐ yes ☒ no
Verified and Acknowledged Examiners's initials DC

ATTORNEY DOCKET NO

END920010050US1(14647)

TITLE : ASIC architecture for active-compensation of a programmable impedance I/O

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

BEST AVAILABLE COPY

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)